



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

April 21, 2006
Date

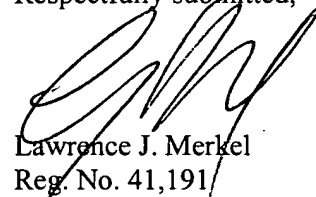
1. This Information Disclosure Statement is submitted:
- a. ☐ within 3 months of the filing date of a national application other than a continued prosecution application under § 1.53(d);

- ☐ within 3 months of the date of entry of the national stage as set forth in § 1.491 in an International application;
- ☒ before the mailing date of a first Office Action on the merits; or
- ☐ before the mailing of a first Office Action after the filing of a request for continued examination under § 1.114.
- b. ☐ after the events of above paragraph 1a and prior to the mailing date of a final Office Action or Notice of Allowance, and thus: ☐ the certification of paragraph 2 below is provided, or ☐ a fee of \$180.00 is enclosed.
- c. ☐ after the mailing date of a final Office Action or a Notice of Allowance and prior to payment of the issue fee, and thus: the certification of paragraph 2 below is provided and a fee of \$180.00 is enclosed.
2. It is hereby certified:
- ☐ that each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart application and that this communication was not received by any individual designated in § 1.56(c) more than thirty (30) days prior to the filing of the information disclosure statement;
- ☐ that each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three (3) months prior to the filing of the Statement; or
- ☐ that no item of information contained in the Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the person signing the certification after making reasonable inquiry, was known to any individual designated in § 1.56 (c) more than three (3) months prior to the filing of the Statement.
3. ☐ Consideration of the following additional information (including any co-pending or abandoned U.S. applications, prior uses and/or sales, etc.) is requested:
4. For each non-English language reference listed on the attached Form PTO-1449:
- ☐ reference is made to an English language translation submitted herewith, and/or
- ☐ reference is made to a foreign patent office search report (in the English language) submitted herewith, and/or
- ☐ reference is made to an English language translation of a foreign patent office search report submitted herewith, and/or
- ☐ reference is made to the concise explanation contained in the specification of the present application at page(s) _____, and/or
- ☐ reference is made to the concise explanation set forth below:

5. ☐ Applicant also offers the following comments for the Examiner's consideration:
6. ☐ Also enclosed is a copy of a foreign search report citing these references.
7. ☐ The listed documents were brought to the attention of the Applicant(s) after payment of the issue fee in the captioned case. The documents were cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement. Applicant(s) request this Information Disclosure Statement and attached Form PTO-1449 be placed in the file of the captioned application.
8. ☐ Applicant(s) requests that the Information Disclosure Statement and attached Form PTO-1449 and references, which are being filed before the grant of the patent and pursuant to 37 C.F.R. § 1.97(i), be placed in the file of the captioned application.

If any required fees are missing, the Commissioner is authorized to charge said fees to Meyertons, Hood, Kivlin, Kowert & Goetzel, P.C. Deposit Account No. 501505/5681-67300/LJM.

Respectfully submitted,



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Date: April 21, 2006

Form PTO-1449 (modified) List of Patents and Publications For Applicant's Information Disclosure Statement (Use several sheets if necessary)			ATTY. DKT. NO. 5681-67300 APPLICANT: Robert E. Cypher FILING DATE: October 30, 2003			SERIAL NO. 10/696,891 GROUP: 2131	
<div style="position: relative; height: 100px;"> <div style="position: absolute; top: 0; left: 0; width: 100%; height: 100%; border: 1px solid black; border-radius: 50%; text-align: center; line-height: 100px; font-size: 20px; font-weight: bold;"> OFFICE APR 27 2006 PATENT & TRADEMARK </div> </div> U.S. PATENT DOCUMENTS							
EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)							
	F1	J. Zweig, et al., "TCP Alternate Checksum Options," March 1990, 5 pages.					
	F2	Craig Partridge, et al., "Performance of Checksums and CRCs over Real Data," 1995 ACM, pp. 68-76.					
	F3	Jonathan Stone, et al., "Performance of Checksums and CRC's over Real Data, 1998 IEEE, ACM Transaction on Networking, Vol. 6, No. 5, pp. 529-543.					
	F4	Anh Vu, "Error Detection and Error Correction," October 27, 1997, 7 pages.					
	F5	Anastase Nakassis, "Fletcher's Error Detection Algorithm: How to Implement it Efficiently and How to Avoid the Most Common Pitfalls," ACM SIGCOMM Computer Communication Review, Vol. 18, Issue 5, October 1998, pp. 63-88					
	F6	Anarchriz, "CRC and How to Reverse it: A CRC Tutorial & The c001 Way to Reverse CRC," April 1999, 14 pages.					
	F7	"Cyclic Redundancy Check," August 2, 1997, 1 pages.					
	F8	Eric Smith, "Memory Error Detection and Correction: An Introduction to Parity and ECC," 8/23/96, 2 pages.					
	F9	Timothy Hsu, "How to Select System Memory," January 9, 1999, 13 pages.					
	F10	"Memory Tutorial," 1998, 3 pages					
	F11	Satran, Sheinwald Informational, "iSCSI CRC Considerations," May 7, 2001, 18 pages.					
	F12	"A Short Spiel on 1-Bit Error Correction Codes," printed 8/11/01, 2 pages.					
	F13	Rick Kelly, "Error Management Using DesignWare CRC and ECC," 2001 Synopsys, Inc., 5 pages.					
	F14	ZDNet UK, "Parity RAM and ECC," PC Magazine Guide to Servers, 1999, 1 page.					
	F15	"Before you start - Chapter 6: Break and Verify," © Laurence Soucy 1997/2001, 1 page.					
	F16	"A Painless Guide to CRC Error Dection Algorithms," Version 3.00, 9/24/96, 19 pages.					
	F17	"Error Control Systems for Digital Communication and Storage," Stephen B. Wicker, Prentice Hall 1995, pages. 100-123.					

EXAMINER:

DATE CONSIDERED:

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the patent owner.